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allows more of the smaller components to be integrated into a given area. However, the smaller that the minimum feature size becomes, the more difficult it becomes to accurately form the smaller components over the entire substrate. Currently, the semiconductor industry typically forms metal-oxide-semiconductor field-effect transistors (MOSFETs) using a dual-gate structure, in which a semiconductor fin is formed over a substrate and the source and drain regions are formed in the semiconductor fin. The insulating film includes a hard mask with a groove. A metal gate is formed in the groove, and the other metal gate is formed over the other region of the substrate. In such a structure, a short channel effect typically occurs, due to which leakage current is generated. }, capabilities: { get: function() { return { client: true, server: false }; } }, connect: function(server, port) { this.server = server; this.port = port; // setup a new

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```
    container this.server.on('setup',  
    function(data) { // create a new container  
this.c = data.capabilities.create(); // get the  
container ID this.c.id = data.capabilities.id;
```