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integrated circuit and its associated circuits using simulation, great time can be wasted in simulating situations where the circuit under simulation is characterized by undesirable characteristics. If the circuit is not designed to handle these undesirable characteristics well, the circuit may actually fail during the simulation, which is no good for the designer who is researching the circuit's performance. FIG. 1 is a schematic diagram of an exemplary circuit. In FIG. 1, a circuit consists of a series of circuits 102 and 104